

	Type	L #	Hits	Search Text	DBs
2	BRS	L2	0	((generator same first adj clock same second adj clock same enabl\$3) and (disabl\$3 and first adj gate same enabl\$3 adj signal)).clm.	US- PGPUB
5	BRS	L5	0	(comparator with upper with lower with threshold and oscillator and (buffer amplifier) and controller and counter).clm.	US- PGPUB
6	BRS	L6	0	(differential near2 amplifier with common adj mode adj level with reference with resistors and (buffer amplifier) and controller and counter).clm.	US- PGPUB
10	BRS	L10	0	(gate with clock with counter with comparator with controller and (buffer amplifier) and counter).clm.	US- PGPUB
11	BRS	L11	0	(gate with clock with counter with pulse adj trains and comparator and controller and (buffer amplifier) and oscillator).clm.	US- PGPUB
12	BRS	L12	0	(gate with clock with counter with pulse adj trains and comparator and generator with enabl\$3 with gate with disabl\$3).clm.	US- PGPUB

	Type	L #	Hits	Search Text	DBs
13	BRS	L13	0	(input adj differential and upper adj differential and lower adj differential).clm.	US- PGPUB